

U.S. Serial No.: 09/922,319

REMARKS

The above amendment has been prepared in accordance with the revised format now permitted as a waiver to the requirements of 37 CFR 1.121. The above amendment is considered to address the concerns raised by the petitions examiner in considering Applicant's petition to accept an unintentionally delayed claim of priority.

Applicant respectfully submits that the application is in condition for allowance, an early indication thereof is respectfully solicited. Should the Examiner have any questions or concerns regarding the amendments presented herein, the Examiner is invited to contact the undersigned representative of the Applicant.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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Date: May 21, 2003

SUBSTITUTE SPECIFICATION

Related Applications

This application is a continuation of U.S. Patent Application No. 09/382,402, filed August 24, 1999, now U.S. Patent No. 6,295,599, which is a continuation-in-part of U.S. Patent Application No. 09/169,963, filed October 13, 1998, now U.S. Patent No. 6,006,318, which is a continuation of U.S. Patent Application No. 08/754,827, filed November 22, 1996 now U.S. Patent No. 5,822,603, which is a divisional of U.S. Patent Application No. 08/516,036, filed August 16, 1995 now U.S. Patent No. 5,742,840.

Microfiche Appendix

Include herewith as an Appendix are 5 sheets of microfiche of 63 frames each.

Field of the Invention

The present invention relates to general purpose processor architectures, and particularly relates to wide operand architectures.

REFERENCE TO A "SEQUENCE LISTING," A TABLE, OR A COMPUTER PROGRAM LISTING APPENDIX SUBMITTED ON A COMPACT DISK

This application includes an appendix, submitted herewith in duplicate on compact disks labeled as "Copy 1" and "Copy 2." The contents of the compact disks are hereby incorporated by reference.

BACKGROUND OF THE INVENTION

The performance level of a processor, and particularly a general purpose processor, can be estimated from the multiple of a plurality of interdependent factors: clock rate, gates per clock, number of operands, operand and data path width, and operand and data path partitioning. Clock rate is largely influenced by the choice of circuit and logic technology, but is also

**PROGRAMMABLE PROCESSOR WITH GROUP FLOATING-POINT
OPERATIONS**

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SUBSTITUTE SPECIFICATION

Related Applications

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